PTO-1449 REPRODUCED	ATTORNEY DOCKET NO. DEC99-55 (PD99-2662)	APPLICATION NO. 09/411,194
INFORMATION DISCLOSURE CITATION **IN AN APPLICATION** February 22, 2000 *(Use several sheets if necessary)*	APPLICANT Joel S. Emer *et al.*	
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U.S. PATENT DOCUMENTS

EXAM-INER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
CSK	AA	5,790,851	08-04-98	Frank *et al.* 709/104	395	6	
CSK	AB	5,524,247	06-04-96	Mizuno 710/200	395	726	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
	AL						
	AM						

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

CSK	AR	Tullsen, D. M., *et al.*, "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor," Paper Presented at the Proceedings of the 23rd Annual International Symposium on Computer Architecture, Philadelphia, PA, (May 1996).
CSK	AS	Lo, J.L., *et al.*, "Converting Thread-Level Parallelism to Instruction-level Parallelism via Simultaneous Multithreading," Version of this Paper to appear in ACM Transactions on Computer Systems, pp. 1-25 (August 1997).
CSK	AT	Tullsen, D.M., *et al.*, "Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor," UCSD CSE Technical Report (CS98-587) and UW CSE Technical Report (UW-CSE-98-06-02) (June 1998).
CSK	AU	Tullsen, D. M. *et al.*, "Simultaneous Multithreading: Maximizing On-Chip Parallelism," Paper Presented at the Proceedings of the 22nd Annual International Symposium on Computer Architecture, Santa Margherita Ligure, Italy (June, 1995).
KSK	AV	Eggers, S. J. *et al.*, "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, pps. 12-19, (September/October 1997).

EXAMINER Ken S. Kim	DATE CONSIDERED 4-22-02